

FIG. 1

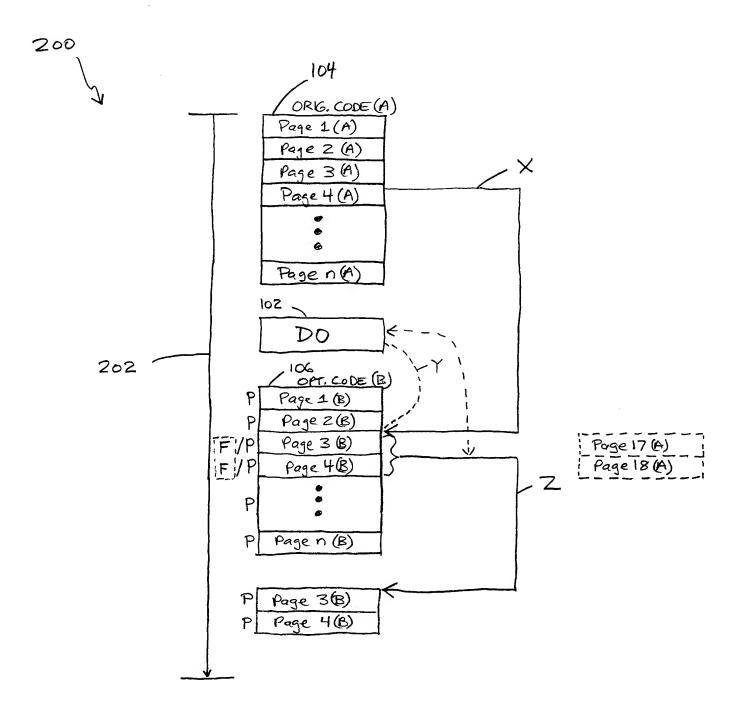


FIG. Z

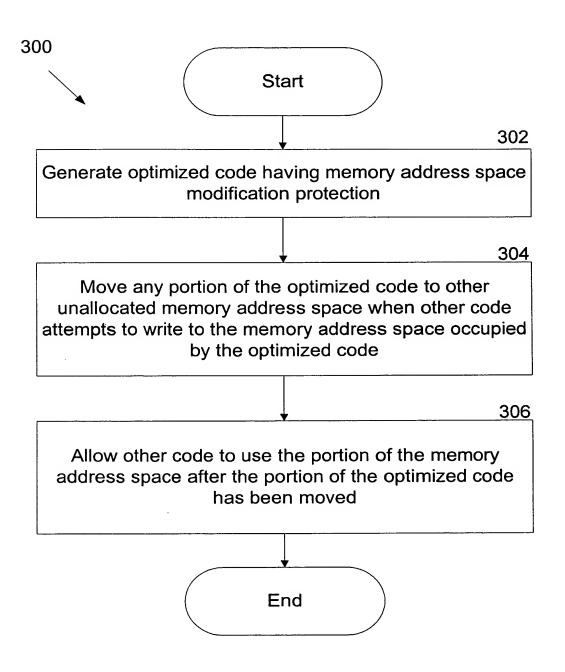
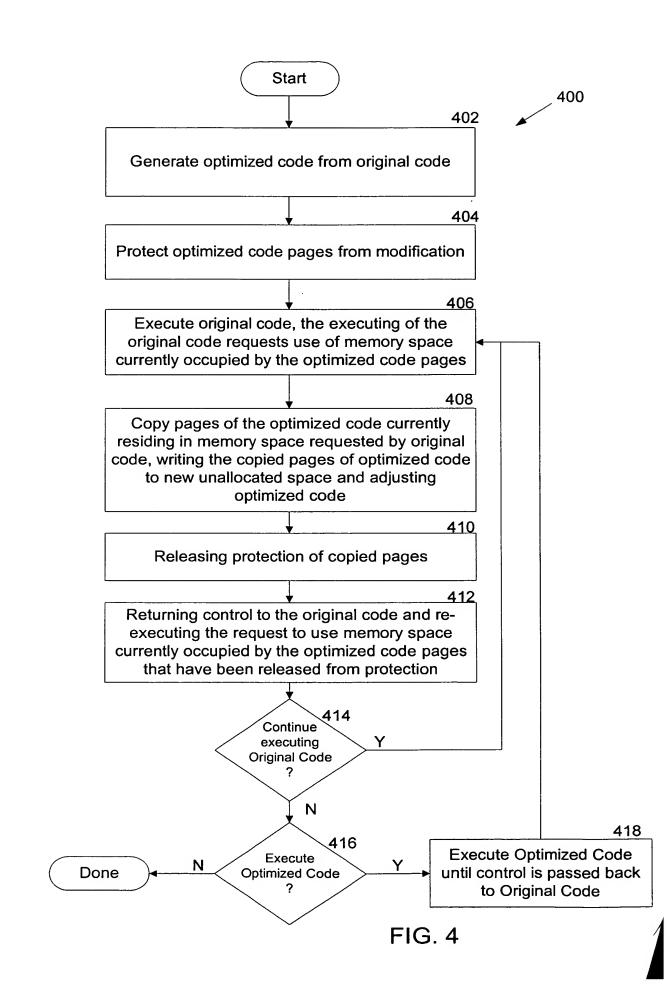


FIG. 3



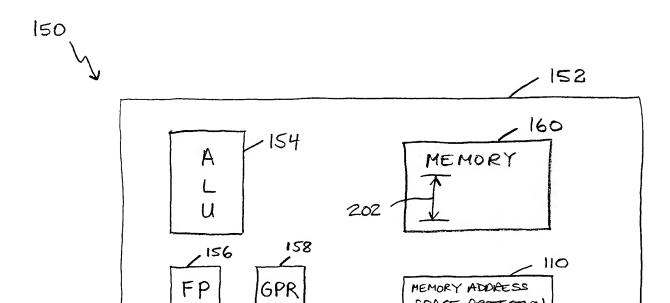


FIG. 5

CPU

SPACE PROTECTION LOGIC